NAPAC25 - North American Particle Accelerator Conference 2025



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Ultrafast Switching Utilizing an IVA Topology for Chopper Applications

Recent trends in power electronics indicate increas-ing demand for fast response switching networks with sub nanosecond switching speed at a variety of volt-ages. Gate driving networks meet the desired switching speeds using COTS (Commercial Off-The Shelf) parts. This work describes an IVA (Inductive Voltage Adder) system capable of switching in the single digits of ns with a projected voltage output of 2 kV, using a gate driving topology to drive GaN (Gallium Nitride) HEMTs (High Electron Mobility Transistor). These rapid switching systems are proposed to be used in the LAMP (LANSCE Accelerator Modernization Project) chopper to effectively produce clean beam to select target stations, producing the needed output.

Please consider my poster for contributed oral presentation

No

Would you like to submit this poster in student poster session on Sunday (August 10th)

Yes

Footnotes

Funding Agency

I have read and accept the Privacy Policy Statement

Yes

Author:HANSZ, Kyle (Los Alamos National Laboratory)Presenter:HANSZ, Kyle (Los Alamos National Laboratory)Session Classification:MC6

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