

## **Session Program**

**20-26 Sept 2025**



# **ICALEPCS 2025 - The 20th International Conference on Accelerator and Large Experimental Physics Control Systems**

## ***MODR MC05 FPGA and Embedded Systems***

Palmer House Hilton Chicago  
17 East Monroe Street Chicago, IL 60603, United States of America

# Monday 22 September

16:00

## MODR MC05 FPGA and Embedded Systems

**Session** |

**Location:** Palmer House Hilton Chicago, Red Lacquer Room, 17 East Monroe Street Chicago, IL 60603, United States of America |

**Conveners:** Michael Costanzo, Evangelia Gousiou, Scott Cogan

16:00–16:15

### Readout of the FPGA-based global trigger system of the ATLAS experiment

**Speaker**

Mr Tong Xu

16:15–16:30

### PandABox II: A collaborative platform designed for future upgrades

**Speaker**

Yves-Marie Abiven

16:30–16:45

### Modernizing embedded controllers at the NIF

**Speaker**

Adrian Barnes

16:45–17:00

### Distributed I/O Tier as a reference platform for harnessing system-on-chips in CERN's control system: gateway design, build system, and software services

**Speaker**

Alen Arias Vazquez

17:00–17:15

### Next generation direct RF sampling LLRF control and monitoring system for linear accelerators

**Speaker**

Chao Liu

17:15–17:30

### Real-time FPGA-based control architecture for high-dynamic mechatronic systems at SIRIUS

**Speaker**

Telles René Silva Soares

17:30–17:45

### Design of a standardized FPGA architecture for EIC common platform daughtercards

**Speaker**

Paul Bachek

17:45