



# HIGH PERFORMANCE GENERIC BEAM DIAGNOSTIC SIGNAL PROCESSOR FOR SHINE

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#### •Abstract

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A generic signal processor has been developed for beam diagnostic system in SHINE. The stand-alone processor is used for the signal processing of stripline BPM, cavity BPM, cold button BPM, beam arrival measurement, bunch length measurement and other diagnostic systems. The main core is a SoC FPGA, which contains both quad-core ARM and FPGA on a chip. The ARM runs LINUX OS and EPICS IOC, and FPGA performs peripheral interfaces and high-speed real-time signal processing. An FMC carrier ADC board is mounted, which can sample 4 channels input signal with a maxi-mum sampling rate of 1GSPS. The processor is equipped with a White Rabbit timing card, which can realize 1MHz high repetition rate synchronous measurement. Lab test results and on-line beam tests prove that the processor has high performance. This paper will introduce the processor development and applications on SHINE.

## Generic beam diagnostic signal processor

The processor is designed as a 1U height standalone device. The core component is a Xilinx FPGA, an ADC daughter board connected through FMC HPC interface, and a White Rabbit timing (WRN) daughter board connected through FMC LPC interface.





#### Application on SHINE and Performance evaluation







In October 2023, the processors were deployed in the injector section of SHINE.



### Conclusion

We have completed the installation of the processors in the injector section and achieved their application. During beam commissioning, we have measured the first beam in SHINE and confirming a high repetition rate of 1 MHz. To date, 270 units have been delivered. This lays the groundwork for the next step in beam commissioning and testing. Next, we will proceed with installation and testing in other sections of SHINE.

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