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A full digital beam position and phase measurement signal processing algorithm based on FPGA designed for linear accelerator

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A digital beam position and phase measurement (BPPM) signal processing algorithm has been designed and implemented. This designed structure has been conceived to be patterned, charged particle independent, and reconfigurable so that it could eventually be used in different kinds of linear accelerators. The BPPM digital signal processing algorithm includes the measurement both of the fundamental and the second harmonic signal simultaneously. Direct under-sampling beam signals by using five ADCs as well as the reference signal, the position, and phase information is retrieved by using the in-phase and quadrature (IQ) demodulation method. The processing algorithm units consist of three parts. The first part is the quadrature I and Q signals generated directly by the Hilbert transform algorithm based on the reference signal in digital, including the 1st and 2nd IQ groups. The second part is the digital down converter and the phase and position bandwidth online reconfigure unit, which could realize more than one kind of bandwidth and data rates result. The third part is a multi-coefficient mapping position correction module to realize the beam position measurement in the nonlinear region. The laboratory test result shows the achieved phase resolution is better than 0.03 degree and position resolution better than 7 μm under the input signal less than 45 dBm. This design pattern algorithm BPPM prototype processor has been used in different kinds of linear accelerators at IMP.

Footnotes

Funding Agency

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